

REMARKS

Claims 1-16, 33 and 34 are pending in this application.

By this Amendment, claims 17-32 are canceled due to the finality of the Restriction Requirement. Claims 1, 4, 5, 8, 9, 12, 13 and 16 are withdrawn due to the finality of the Election of Species Requirement. Claim 2 is amended to recite additional features as described in the specification at, for example, Figs. 2, 4 and 5; and page 17, lines 6-9. Claims 1, 3 and 4 are similarly amended.

Claims 33 and 34 are added to recite additional features disclosed in the specification at, for example, Fig. 5; page 15, lines 20-22; page 17, lines 12-15; and page 17, line 27 through page 18, line 4.

The title is amended in view of the cancellation of claims 17-32.

Reconsideration of the application is respectfully requested.

The Examiner is respectfully requested to acknowledge consideration of the references listed on, and to return an initialed copy of, the Form PTO-1449 submitted with the May 23, 2005 Information Disclosure Statement.

The Office Action rejects claims 2, 3, 6, 7, 10, 11, 14 and 15 under 35 U.S.C. §102(b) over U.S. Patent No. 6,172,721 to Murade et al. ("Murade"). This rejection is respectfully traversed.

Murade discloses a scanning line 3a and a capacitor line 3b. See Fig. 3. The Office Action appears to assert that Murade's scanning line 3a and capacitor line 3b correspond to the plurality of layers of an interconnect layer recited in claims 2 and 3. The Office Action also appears to assert that Murade's scanning line 3a and capacitor line 3b correspond to the first and second interconnecting patterns, respectively.

Claim 2 is amended to clarify that the plurality of layers are formed one over another at different levels in a thickness direction of the substrate, as outlined above. Claim 3 is similarly amended. Murade does not disclose or suggest these additional features.

In particular, Murade's scanning line 3a and capacitor line 3b are formed at the same level in a thickness direction of substrate 10. See Fig. 3 of Murade. The scanning line 3a and capacitor line 3b are not formed over one another, and are not formed at different levels in the thickness direction of the substrate 10. Therefore, Murade does not disclose or suggest an interconnect layer formed of a plurality of layers, the plurality of layers being formed one over another at different levels in a thickness direction of the substrate, as recited in claims 2 and 3; much less "the first interconnecting pattern being positioned in a first layer among the plurality of layers forming the interconnect layer, the second interconnecting pattern being positioned in a second layer among the plurality of layers forming the interconnect layer," as recited in claim 2, or "wherein first and second interconnecting patterns positioned respectively in first and second layers among the plurality of layers forming the interconnect layer have portions extending parallel to each other under each of the electrodes, and the parallel extending portions are formed not to overlap each other," as recited in claim 3.

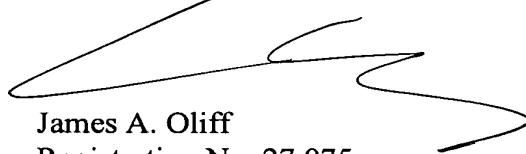
For at least the above reasons, Murade does not disclose each and every element recited in claims 2 and 3, and claims 6, 7, 10, 11, 14 and 15 depending therefrom. Accordingly, withdrawal of the rejection of claims 2, 3, 6, 7, 10, 11, 14 and 15 under 35 U.S.C. §102(b) is respectfully requested.

Claims 33 and 34 are patentable over Murade at least in view of the patentability of claims 2 and 3, from which they respectively depend, as well as for additional features they recite.

In view of the foregoing, it is respectfully submitted that this application is in condition for allowance. Favorable reconsideration and prompt allowance are earnestly solicited.

Should the Examiner believe that anything further would be desirable in order to place this application in even better condition for allowance, the Examiner is invited to contact the undersigned at the telephone number set forth below.

Respectfully submitted,



James A. Oliff
Registration No. 27,075

Gang Luo
Registration No. 50,559

JAO:GXL/eks

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OLIFF & BERRIDGE, PLC
P.O. Box 19928
Alexandria, Virginia 22320
Telephone: (703) 836-6400

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